



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Yasuhiko HONDA

Serial No: Not assigned

Filed: October 22, 2003

For: Semiconductor Memory Device Having a Burst  
Continuous Read Function

Art Unit: Not assigned

Examiner: Not assigned

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I hereby certify that

- ☒ two copies of a letter of transmittal
- ☒ patent application ( 45 page(s) of specification; 20 claim(s); 1 page(s) of abstract
- ☒ 12 sheet(s) of formal drawings
- ☒ certified copy of Japanese patent application No. 2003-205379 which was filed  
August 1, 2003 from which priority is claimed in the subject case pursuant to 35 U.S.C.  
§ 119
- ☒ Information Disclosure Statement with 2 references
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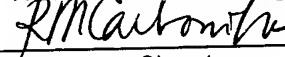
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